Compact Modeling of MicroRing-based RF/mmWave-to-Optical Modulators

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1. Introduction

The need for growing wireless capacity will utilize more spectrum in the centimeter wave (cmWave, 6 to 30 GHz) and millimeter wave (mmWave, 30-100 GHz) range, and deploy small cells and heterogeneous networks to provide a Terabit/s/km² capacity by 2030 [1]. As a result, flexible cmWave and mmWave front-ends with massive beamforming, that can seamlessly operate over a large range of spectrum become indispensable. Further, the high data rates transmitted over these links necessitate investigation of architectures that transcend the data capacity limitations of traditional electronic integrated circuits (ICs) and copper-based links.

Technology development leveraging RF photonics has thus far largely focused on isolated implementation of optical modulators and detectors [2]. Exploitation of silicon-based integrated optoelectronics can alleviate challenges with signal processing and distribution of mmWave waveforms, e.g. using Radio-over-Fiber links. These potential opportunities motivate system designers to investigate linearized electrooptic modulators that can convert RF/mmWave signals into optical domain with high dynamic range and SFDR. Breakthrough in this area will lead to transformative system-level functionality and reconfigurability over multi-GHz range in the last 50 feet of wireless, and potentially eliminate copper-based links in the front- and back-haul networks.

2. Linearized CMOS Photonics Modulators

Figure 2. Envisioned RF/mmWave photonics transmitter architecture.

3. Research Approach

Ring Modulators: Compact ring modulators (~30μm radius) eliminate the need for a traveling-wave type CMOS driver for the Mach-Zehnder modulators resulting in a simple lumped capacitor interface. Depletion-mode (or reversed-biased) pn-junction based phase shifters available in SOI Photonic processes are used in the ring modulator to realize GHz electro-optic bandwidth. Typically, we design the optical components using FDTD, Modes and optical design software packages. However, compact modeling of ring-based structures is important as it allows co-simulation of photonic components with the interfacing electronic circuits in Cadence circuit design environment. Modeling challenges include accurate capture of transient response with electrical and optical time-constants. A dynamic model that treats the ring modulator as a voltage controlled oscillator, and given by equations [4]:

\[
\frac{\partial \Delta \phi}{\partial t} = \frac{1}{2} \left( \frac{w_0}{\lambda_0} - \frac{w_0}{\lambda_0} \right) \delta(t) + \frac{2}{\lambda_0} \beta E_{in} e^{j (\omega_0 t - \lambda x)} \\
E_{out}(t) = E_{in} e^{j (\omega_0 t - \lambda x)} + j \mu E_{in} e^{j (\omega_0 t - \lambda x)}
\]

Here, \(\delta(t)\) is the energy amplitude circulating in the ring, \(w_0 = \frac{\lambda_0}{2}\) is the laser carrier frequency with \(\lambda_0 = 1550\text{nm}\), \(\omega_0\) is the resonant angular frequency of the ring, \(E_c\) and \(E_{in}\) are the input laser intensity. The time-constants \(\tau_r(t)\) and \(\tau_e(t)\) represent the energy amplitude decay in the ring and the coupling losses respectively.

The coupling factor \(\mu\) relates to the coupling coefficient as \(\mu = \sqrt{\frac{\tau_e}{\tau_r}}\), where \(\tau_e\) is the group velocity and \(R\) is the ring radius [4]. Here, \(E_c e^{j (\omega_0 t - \lambda x)}\) is the frequency-translated analytic model which significantly speeds up the differential equation solution in Spectre.

Furthermore, \(\tau_r(t) = f_{VOC} + \gamma V_i(t)\) has been curve-fitted using simulation (and experimental) results for depletion-mode ring modulator for input DC bias, \(V_{DC}\) and electrical signal \(v_i(t)\).

Figure 3. Micrograph of a fabricated 130-nm SOI photonic chip with Mach-Zehnder and Ring modulators, filter banks, and detectors.

4. Research Progress

Compact Linearized EO Modulator: We are investigating compact SOI-photonic modulators, including the self-calibrating ring-assisted Mach-Zehnder modulator (RAMZI) as shown in Figure 6. Here, compact ring modulators (~30 μm radius) in a differential drive structure eliminate the need for a traveling-wave type CMOS driver for the modulator [5], resulting in a simple lumped capacitance interface. Depletion-mode pn-junction based phase shifters are used in the ring modulator to realize >50 GHz electro-optic bandwidth. Compact modeling based simulations exhibit 10dB higher IP3 and 105 dB SFDR.

5. FPGA based Controller

Figure 4. FPGA based controller for the RAMZI modulator.

6. Conclusion and Broader Impact

Classified mmWave photonic modulators with >100 dB SFDR will novel hybrid CMOS photonic system concepts; critical breakthrough required for making the next leap in broadband wireless communication to satisfy the ever growing demand for data capacity. Low cost and small form-factor mmWave photonic transceivers developed using the proposed approach can potentially transform the U.S. wireless and semiconductor industry by enabling multi-beam and multi-Gbps data rates.

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References


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